

Day : Wednesday

Date: 11/15/2006

Time: 17:15:07

 **PALM INTRANET**

Inventor Information for 10/617687

Inventor Name	City	State/Country
<u>HABUKA, TOSHIHITO</u>	TAMAMURA	JAPAN
<u>NODA, MASAKI</u>	CHIGASAKI	JAPAN
<u>NOGAMI, HIROSHI</u>	HUJISAWA	JAPAN
<u>HORI, TOYOKAZU</u>	KODAIRA	JAPAN
<u>MATSUURA, TATSUJI</u>	TOKYO	JAPAN
<u>HORI, KAZUAKI</u>	YOKOHAMA	JAPAN
<u>INOKAWA, NAOTO</u>	TAKASAKI	JAPAN

[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity/Reexam](#)[Foreign](#)

Search Another: Application#

or Patent#

PCT /

/

or PG PUBS #

Attorney Docket #

Bar Code #

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Wednesday

Date: 11/15/2006

Time: 17:15:13


PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = HABUKA

First Name = TOSHIHITO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>06321604</u>	Not Issued	161	11/16/1981	SEMICONDUCTOR INTEGRATED CIRCUIT FOR ELIMINATING CROSS TALK	HABUKA, TOSHIHITO
<u>08161810</u>	Not Issued	168	12/06/1993	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A TEST OUTPUT DATA PARITY CHECK CIRCUIT	HABUKA, TOSHIHITO
<u>08461187</u>	Not Issued	161	06/05/1995	RECORDED DATA REPRODUCING DEVICE	HABUKA, TOSHIHITO
<u>10617687</u>	Not Issued	30	07/14/2003	Semiconductor integrated circuit device, wireless LAN system, and auto gain control system	HABUKA, TOSHIHITO
<u>10809898</u>	Not Issued	30	03/26/2004	OFDM demodulation method and semiconductor integrated circuit device	HABUKA, TOSHIHITO
<u>10855596</u>	Not Issued	30	05/28/2004	Communication semiconductor integrated circuit, radio communication system, and adjustment method of gain and offset	HABUKA, TOSHIHITO
<u>11033384</u>	Not Issued	30	01/12/2005	Radio communication system and radio-frequency integrated circuit	HABUKA, TOSHIHITO

Inventor Search Completed: No Records to Display.

Search Another: Inventor
Last Name
First Name

HABUKA
TOSHIHITO
Search

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Wednesday

Date: 11/15/2006

Time: 17:15:32


PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = NODA

First Name = MASAKI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
06286511	4435688	150	07/24/1981	FET MICROWAVE OSCILLATOR BEING FREQUENCY STABILIZED BY CAPACTIVE REACTANCE MICRO-STRIP STUB LINE	NODA, MASAKI
06348016	4417279	150	02/11/1982	FM TELEVISION SIGNAL RECEIVING CIRCUIT	NODA, MASAKI
06406567	4491809	150	08/09/1982	MATCHING CIRCUIT FOR A PRE-AMPLIFIER OF SHF BAND TELEVISION SIGNAL RECEIVER	NODA, MASAKI
06508467	4589016	150	06/28/1983	FM TELEVISION SIGNAL RECEIVING CIRCUIT	NODA, MASAKI
06827626	4731875	150	02/10/1986	FREQUENCY CONVERTER USED FOR A RECEIVER WHICH RECEIVES SIGNALS OF A UHF BAND OR A MICROWAVE BAND	NODA, MASAKI
06837040	RE32527	150	03/06/1986	FET MICROWAVE OSCILLATOR BEING FREQUENCY STABILIZED BY CAPACTIVE REACTANCE MICRO-STRIP STUB LINE	NODA, MASAKI
07232327	4853731	150	08/12/1988	AUTOFOCUS CAMERA HAVING AUTOMATIC FOCUS ADJUSTMENT APPARATUS	NODA, MASAKI
07882276	Not Issued	166	05/13/1992	WIDE-BAND RECEIVING APPARATUS FOR SELECTIVELY RECEIVING RF-SIGNALS OF TWO OR MORE DIFFERENT FREQUENCY BANDS	NODA, MASAKI
07883466	5260671	250	05/15/1992	RECEIVING CIRCUIT FOR	NODA, MASAKI

				DEMODULATING AN ANGLE MODULATED SIGNAL	
<u>08012843</u>	<u>5382901</u>	250	02/02/1993	OPTICAL MAGNETIC FIELD SENSOR CAPLE OF PRECISE MEASUREMENT WITHOUT TEMPERATURE INDUCED ERRORS	NODA, MASAKI
<u>08145101</u>	<u>5442122</u>	150	11/03/1993	DIBENZOSUBERYL AND DIBENZOSUBERENYL DERIVATIVES	NODA, MASAKI
<u>08274731</u>	Not Issued	161	07/18/1994	TEST PATTERN GENERATING APPARATUS AND METHOD	NODA, MASAKI
<u>08397735</u>	<u>5630214</u>	150	03/02/1995	WIDE-BAND RECEIVING APPARATUS WITH LOCAL OSCILLATING CIRCUIT	NODA, MASAKI
<u>09340484</u>	<u>6414219</u>	150	06/30/1999	OSTEOPONTIN KNOCK-OUT MOUSE AND METHODS OF USE THEREOF	NODA, MASAKI
<u>09643248</u>	<u>6437899</u>	150	08/22/2000	OPTO-ELECTRIC CONVERSION SEMICONDUCTOR DEVICE	NODA, MASAKI
<u>09764442</u>	<u>6549321</u>	150	01/19/2001	OPTICAL MODULATOR	NODA, MASAKI
<u>09972917</u>	Not Issued	164	10/10/2001	OPTICAL MODULE, OPTICAL TRANSMITTER AND OPTICAL RECEIVER	NODA, MASAKI
<u>10188884</u>	Not Issued	120	07/02/2002	Osteopontin knock-out mouse and methods of use thereof	NODA, MASAKI
<u>10473134</u>	Not Issued	71	05/20/2004	Anti-osteopontin antibody and use thereof	NODA, MASAKI
<u>10489776</u>	Not Issued	71	10/05/2004	THERAPEUTIC AGENTS FOR OSTEOPENIA	NODA, MASAKI
<u>10543390</u>	Not Issued	25	07/26/2005	Preamplifier	NODA, MASAKI
<u>10566240</u>	Not Issued	30	04/13/2006	Circuit for varying gain of preamplifier	NODA, MASAKI
<u>10617687</u>	Not Issued	30	07/14/2003	Semiconductor integrated circuit device, wireless LAN system, and auto gain control system	NODA, MASAKI
<u>10832316</u>	Not Issued	30	04/27/2004	Optical transmitter and optical module	NODA, MASAKI
<u>10874459</u>	Not Issued	41	06/22/2004	Multimode wireless terminal and wireless transmitter-receiver unit	NODA, MASAKI
<u>60091200</u>	Not	159	06/30/1998	OSTEOPONTIN KNOCK-OUT	NODA, MASAKI

	Issued		MOUSE AND METHODS OF USE THEREOF	
--	--------	--	-------------------------------------	--

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	<input type="button" value="Search"/>
	NODA	MASAKI	

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Wednesday

Date: 11/15/2006

Time: 17:15:40

**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = NOGAMI

First Name = HIROSHI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>07480641</u>	<u>5276670</u>	150	02/15/1990	PHASE CHANGE OPTICAL DISK AND METHOD FOR USING THE SAME	NOGAMI, HIROSHI
<u>08558794</u>	Not Issued	166	11/15/1995	PLASMA PROCESSING METHOD	NOGAMI, HIROSHI
<u>08795197</u>	<u>6436304</u>	150	02/05/1997	PLASMA PROCESSING METHOD	NOGAMI, HIROSHI
<u>09255852</u>	<u>6245396</u>	150	02/23/1999	CVD APPARATUS AND METHOD OF USING SAME	NOGAMI, HIROSHI
<u>09820149</u>	<u>6851384</u>	150	03/28/2001	REMOTE PLASMA APPARATUS FOR PROCESSING SUBSTRATE WITH TWO TYPES OF GASES	NOGAMI, HIROSHI
<u>09863338</u>	<u>6892669</u>	150	05/24/2001	CVD APPARATUS	NOGAMI, HIROSHI
<u>10043190</u>	<u>6758224</u>	150	01/14/2002	METHOD OF CLEANING CVD DEVICE	NOGAMI, HIROSHI
<u>10352876</u>	Not Issued	41	01/29/2003	Cryption and decryption communication semiconductor device and recording/reproducing apparatus	NOGAMI, HIROSHI
<u>10475417</u>	Not Issued	41	11/12/2003	Tunning fork type therapeutic utensil	NOGAMI, HIROSHI
<u>10617687</u>	Not Issued	30	07/14/2003	Semiconductor integrated circuit device, wireless LAN system, and auto gain control system	NOGAMI, HIROSHI
<u>10709622</u>	Not Issued	41	05/18/2004	METHOD OF CLEANING A CVD DEVICE	NOGAMI, HIROSHI
<u>10809898</u>	Not Issued	30	03/26/2004	OFDM demodulation method and semiconductor integrated circuit device	NOGAMI, HIROSHI

<u>10978150</u>	Not Issued	41	10/29/2004	Remote plasma apparatus for processing substrate with two types of gases	NOGAMI, HIROSHI
<u>11033384</u>	Not Issued	30	01/12/2005	Radio communication system and radio-frequency integrated circuit	NOGAMI, HIROSHI

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	<input type="button" value="Search"/>
	<input type="text" value="NOGAMI"/>	<input type="text" value="HIROSHI"/>	

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Wednesday

Date: 11/15/2006

Time: 17:15:51


PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = HORI

First Name = TOYOKAZU

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08952653	6185595	150	03/10/1998	DISCRETE COSINE TRANSFORMATION OPERATION CIRCUIT	HORI, TOYOKAZU
09660922	Not Issued	161	09/13/2000	Discrete cosine transformation operation circuit	HORI, TOYOKAZU
09745928	6658511	150	12/26/2000	DATA PROCESSING PROCESSOR	HORI, TOYOKAZU
10617687	Not Issued	30	07/14/2003	Semiconductor integrated circuit device, wireless LAN system, and auto gain control system	HORI, TOYOKAZU
10673851	6944696	150	09/30/2003	DATA PROCESSING PROCESSOR	HORI, TOYOKAZU
10809898	Not Issued	30	03/26/2004	OFDM demodulation method and semiconductor integrated circuit device	HORI, TOYOKAZU
11033384	Not Issued	30	01/12/2005	Radio communication system and radio-frequency integrated circuit	HORI, TOYOKAZU
11037133	Not Issued	25	01/19/2005	Demodulator circuit, radio communication system and communication semiconductor integrated circuit	HORI, TOYOKAZU
11089330	Not Issued	20	03/25/2005	Ultrasonic imaging system and imaging method	HORIIKE, TOYOKAZU

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	<input type="button" value="Search"/>
	<input type="text" value="HORI"/>	<input type="text" value="TOYOKAZU"/>	

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Wednesday

Date: 11/15/2006

Time: 17:16:01

**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = MATSUURA

First Name = TATSUJI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>06911154</u>	<u>4745393</u>	150	09/24/1986	ANALOG-TO-DIGITAL CONVERTER	MATSUURA, TATSUJI
<u>07063476</u>	<u>4825287</u>	150	06/18/1987	DIGITAL VIDEO SIGNAL PROCESSOR	MATSUURA, TATSUJI
<u>07574087</u>	<u>5113090</u>	150	08/29/1990	VOLTAGE COMPARATOR	MATSUURA, TATSUJI
<u>07793574</u>	<u>5194866</u>	250	11/18/1991	HALF-FLASH ANALOG-TO-DIGITAL CONVERTER USING DIFERENTIAL COMPARATORS AND CROSSOVER SWITCHING	MATSUURA, TATSUJI
<u>07819138</u>	Not Issued	161	01/10/1992	AD CONVERTER AND AD CONVERSION METHOD	MATSUURA, TATSUJI
<u>07907524</u>	<u>5274377</u>	250	07/02/1992	PIPELINED A/D CONVERTER	MATSUURA, TATSUJI
<u>08026038</u>	<u>5394148</u>	150	03/04/1993	AD CONVERTER	MATSUURA, TATSUJI
<u>08133218</u>	<u>5534800</u>	150	10/07/1993	SENSE AMPLIFIER, SRAM, AND MICROPROCESSOR	MATSUURA, TATSUJI
<u>08595999</u>	<u>5684486</u>	250	02/06/1996	FLASH A/D CONVERTER	MATSUURA, TATSUJI
<u>09008424</u>	<u>5963064</u>	150	01/16/1998	ELECTRONIC CIRCUIT INCLUDING DIFFERENTIAL CIRCUITRY	MATSUURA, TATSUJI
<u>10080586</u>	<u>6781107</u>	150	02/25/2002	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND IMAGING SYSTEM	MATSUURA, TATSUJI
<u>10086718</u>	<u>6731101</u>	150	03/04/2002	MODULATION SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND TESTING	MATSUURA, TATSUJI

				METHOD FOR OSCILLATION CIRCUIT	
<u>10256149</u>	6677788	150	09/27/2002	SEMICONDUCTOR INTEGRATED CIRCUIT	MATSUURA, TATSUJI
<u>10617687</u>	Not Issued	30	07/14/2003	Semiconductor integrated circuit device, wireless LAN system, and auto gain control system	MATSUURA, TATSUJI
<u>10816925</u>	7122775	150	04/05/2004	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND IMAGING SYSTEM	MATSUURA, TATSUJI
<u>10817042</u>	6841770	150	04/05/2004	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND IMAGING SYSTEM	MATSUURA, TATSUJI
<u>10855596</u>	Not Issued	30	05/28/2004	Communication semiconductor integrated circuit, radio communication system, and adjustment method of gain and offset	MATSUURA, TATSUJI
<u>10855608</u>	Not Issued	61	05/28/2004	IMAGE-SENSOR SIGNAL PROCESSING CIRCUIT	MATSUURA, TATSUJI
<u>11000415</u>	Not Issued	30	12/01/2004	Semiconductor integrated circuit device and imaging system	MATSUURA, TATSUJI
<u>11033384</u>	Not Issued	30	01/12/2005	Radio communication system and radio-frequency integrated circuit	MATSUURA, TATSUJI
<u>11189701</u>	Not Issued	71	07/27/2005	Semiconductor integrated circuit for communication including analog-to-digital conversion circuit	MATSUURA, TATSUJI

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	
	MATSUURA	TATSUJI	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Wednesday

Date: 11/15/2006

Time: 17:16:10


PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = HORI

First Name = KAZUAKI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>06626545</u>	<u>4603695</u>	150	06/29/1984	USE OF MOLDED POLYMERIC MATERIAL FOR PREVENTING ADHESIVE OF VITAL TISSUES	HORI, KAZUAKI
<u>08996207</u>	<u>6215948</u>	150	12/22/1997	MAGNETIC RECORDING/REPRODUCING APPARATUS AND THE SAME EQUIPPED WITH AN IMAGE SENSOR	HORI, KAZUAKI
<u>09774662</u>	<u>6424787</u>	150	02/01/2001	MAGNETIC RECORDING/REPRODUCING APPARATUS AND THE SAME EQUIPPED WITH AN IMAGE SENSOR	HORI, KAZUAKI
<u>09787380</u>	<u>6996377</u>	150	06/18/2001	PLL CIRCUIT AND RADIO COMMUNICATION TERMINAL APPARATUS USING THE SAME	HORI, KAZUAKI
<u>09789566</u>	<u>6384676</u>	150	02/22/2001	SIGNAL PROCESSING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE	HORI, KAZUAKI
<u>10087820</u>	<u>6501330</u>	150	03/05/2002	SIGNAL PROCESSING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE	HORI, KAZUAKI
<u>10148960</u>	Not Issued	98	06/07/2002	TRANSMITTER AND RADIO COMMUNICATION TERMINAL USING THE SAME	HORI, KAZUAKI
<u>10253648</u>	Not Issued	93	09/25/2002	WIRELESS COMMUNICATION RECEIVER	HORI, KAZUAKI
<u>10289331</u>	<u>7095999</u>	150	11/07/2002	SIGNAL PROCESSING SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE	HORI, KAZUAKI

<u>10323819</u>	<u>6750719</u>	150	12/20/2002	COMMUNICATION SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND WIRELESS COMMUNICATION SYSTEM	HORI, KAZUAKI
<u>10375241</u>	<u>6970683</u>	150	02/28/2003	PLL CIRCUIT AND RADIO COMMUNICATION TERMINAL APPARATUS USING THE SAME	HORI, KAZUAKI
<u>10422953</u>	Not Issued	41	04/25/2003	Semiconductor integrated circuit device and wireless communication system	HORI, KAZUAKI
<u>10617687</u>	Not Issued	30	07/14/2003	Semiconductor integrated circuit device, wireless LAN system, and auto gain control system	HORI, KAZUAKI
<u>10685481</u>	<u>7116949</u>	150	10/16/2003	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND WIRELESS COMMUNICATION SYSTEM	HORI, KAZUAKI
<u>10727612</u>	Not Issued	71	12/05/2003	Direct conversion receiver	HORI, KAZUAKI
<u>10739282</u>	Not Issued	93	12/19/2003	DIRECT-CONVERSION TRANSMITTER CIRCUIT AND TRANSCEIVER SYSTEM	HORI, KAZUAKI
<u>10816817</u>	Not Issued	41	04/05/2004	Communication semiconductor integrated circuit device and wireless communication system	HORI, KAZUAKI
<u>10836197</u>	Not Issued	41	05/03/2004	Communication semiconductor integrated circuit device and wireless communication system	HORI, KAZUAKI
<u>11121018</u>	Not Issued	30	05/04/2005	PLL circuit and radio communication terminal apparatus using the same	HORI, KAZUAKI
<u>11176302</u>	Not Issued	25	07/08/2005	Communication semiconductor integrated circuit and radio communication system	HORI, KAZUAKI
<u>11510764</u>	Not Issued	25	08/28/2006	Semiconductor integrated circuit device and wireless communication system	HORI, KAZUAKI
<u>11107857</u>	Not Issued	20	04/18/2005	Cup holder device	HORIGUCHI, KAZUAKI

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	
	HORI	KAZUAKI	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Wednesday

Date: 11/15/2006

Time: 17:16:17

**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = INOKAWA

First Name = NAOTO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10617687</u>	Not Issued	30	07/14/2003	Semiconductor integrated circuit device, wireless LAN system, and auto gain control system	INOKAWA, NAOTO
<u>10809898</u>	Not Issued	30	03/26/2004	OFDM demodulation method and semiconductor integrated circuit device	INOKAWA, NAOTO
<u>10855596</u>	Not Issued	30	05/28/2004	Communication semiconductor integrated circuit, radio communication system, and adjustment method of gain and offset	INOKAWA, NAOTO
<u>11033384</u>	Not Issued	30	01/12/2005	Radio communication system and radio-frequency integrated circuit	INOKAWA, NAOTO

Inventor Search Completed: No Records to Display.

Search Another: Inventor **Last Name** **First Name**

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)